

**GOVERNMENT OF TAMILNADU
DEPARTMENT OF TECHNICAL EDUCATION**

GOVERNMENT POLYTECHNIC COLLEGE, UDHAGAMANDALAM – 643 006

NOTICE INVITING TENDER

E-tender Notice No:742/D/ 2026 Date :27.05.2026.

Online bids are invited through portal [https:// tntenders.gov.in](https://tntenders.gov.in) for purchase of Lab Equipment's for ECE Department of Government Polytechnic College Udhagamandalam from vendors involved in the relevant field. Manual bids shall not be accepted.

Sl. No	Item Name	Quantity required
1.01	FUNCTION GENERATOR	2
1.02	DSO	2
1.03	DC VOLTMETER <ul style="list-style-type: none">• DC Voltmeter ANALOG - Moving coil - (0-30)V, Class 1.0 (± 1 % full-scale) accuracy• DC Digital Voltmeter - 3½ digit LED/LCD display, Measurement range: 0–20 V dc, Accuracy: Class 0.5(± 0.5 % full-scale)• DC Voltmeter ANALOG - Moving coil - (0-1)V, Class 1.0 (± 1 % full-scale) accuracy	Each 1
1.04	DC AMMETER <ul style="list-style-type: none">• DC Ammeter Analog - Moving coil - (0 -500) mA, Class 1.0 (± 1 % full-scale) accuracy• DC Ammeter ANALOG – Moving coil - (0-30) mA, Class 1.0 (± 1 % full-scale) accuracy• DC Ammeter Analog - Moving coil - (0 - 1000) μA, Class 1.0 (± 1 % full-scale) accuracy• DC Ammeter DIGITAL - 3½ digit LED/LCD display, Measurement range: 0–5 A dc, Accuracy: Class 0.5 (± 0.5 % full-scale)	Each 1
1.05	LVDT Sensor Trainer Kit	1
1.06	Auto Transformer	1
1.07	FPGA / SoC Development Board	2

TERMS AND CONDITIONS

1. The Bidders interested in participation the e-Tender must be registered with Tamilnadu e-procurement system portal and also should have Digital Signature Certificate.
2. All the items/ Goods/Equipment/Machinery/Articles should be delivered and installed at the college premises only.
3. Bidders should submit their bids system through online (www.tntenders.gov.in) in prescribed format only.
 - (i) **Technical Bid** – Shall be submitted along with self attested scanned copies of necessary documents in .pdf format.
 - (ii) **Financial Bid** –
 - a) In.xls format only (Excel format)
 - b) Rate per unit should be mentioned.
 - c) Rates quoted by Bidders should be firm &Final.
 - d) Prices should be quoted only in Indian Rupees (INR)
 - e) Price should be inclusive of all freight, Insurance, Packing, Loading & Unloading, Delivery charges etc.
4. Tenders in any other manner will not be accepted.
5. Bidders should have local office in Tamil Nadu within 200 Kms from Ooty.
6. Bidders must not be black listed by Government of Tamil Nadu.
7. The Bidders must have valid
 - a) PAN
 - b) Valid GST Registration Number. (Bids without GST registration Copy will be rejected).
8. Warranty should not be less than 1 year (It will start after the date of Successful Installation).
9. Delivery of the item should be done at Government Polytechnic College, Udhagamandalam - 06
10. Mode of payment through ECS/Cheque (100% payment will be given only after the goods are received in good condition and installation is completed)
11. No Advance payment will be made.
12. As per Tamilnadu Transparency in Tender Act 1998 and Tamilnadu Transparency in Tender Rules 2000.
 - a) Government Polytechnic College Udhagamandalam - 06 reserves the right to modify, reduces or increase the quantity required.
 - b) Withhold any amount for the deficiency in service aspect of the ordered items.

13. The Final decision would be based on the Technical Capacity and pricing of the bidder.
14. The PRINCIPAL, Government Polytechnic College, Udhagamandalam -06, reserves the right, not to accept lowest price or to reject any or all the tenders without assigning any reasons.
15. The PRINCIPAL, Government Polytechnic College Udhagamandalam- 06, reserves the right to call off tender process at any stage without assigning any reasons.
16. For any Corrigendum and addendum please check the above website.
17. Delivery Period : Time is the Essence of this contract. Owing to the tight schedule of the upcoming academic calendar and to prevent any academic loss to the students, the successful bidder must fully deliver, install, check and commission all the ordered lab equipments within exactly 7 (seven) days from the date of issuance of the award of contract (AOC) / purchase order.
18. Vendor Capability: Bidders must ensure they have ready stocks available or a guaranteed supply chain before participating in this tender to meet the strict 7- day timeline.

J. Anusha
27.05.2024

THE PRINCIPAL / TENDER INVITING AUTHORITY
GOVERNMENT POLYTECHNIC COLLEGE
UDHAGAMANDALAM – 643 006.

S. No	College Code	Quantity	Name of the Equipment (As per TANSCHÉ List)	Specification	Approximate Cost	Department where the equipment is required
1	112	2	FUNCTION GENERATOR	<ul style="list-style-type: none"> • Frequency range: 0.1 Hz to 5 MHz. • Display: 4 to 6 digit LED display. • Output waveforms: Sine, triangle, square, \pm pulse, \pm ramp. • Amplitude: 0–20 Vpp. • Rise time: < 100 ns. • Distortion: < 1% within specified frequency range. • Power supply: 230 V ac \pm10%, 50–60 Hz. • Output amplifier protection: Short-circuit and \pm15 V overload protection. • DC offset: 0 to \pm10 V adjustable. • Attenuation: 0 to –60 dB. 	72200	ELECTRONICS AND COMMUNICATION ENGINEERING
2	112	2	DSO	<ul style="list-style-type: none"> • Bandwidth: 50–100 MHz. • Sampling rate: 1 GS/s. • Channels: 2 (minimum). • Power supply: 100–240 V ac, 50–60 Hz. • Automatic measurements: Vpp, Vavg, Vrms, frequency, period, Vmax, Vmin, rise time, fall time, +width, –width, +duty, –duty, etc. • Horizontal scale: 5 ns/div – 50 s/div or equivalent. • Vertical resolution: 8 bits (2 channels simultaneously). • Vertical sensitivity: 1 mV/div – 10 V/div (at input). • Waveform math: +, –, \times, \div, invert, FFT, etc. • Waveform storage: 16 waveforms or equivalent. • Communication interface: USB host and USB interface. • Display: 7-inch color display or equivalent. • Input impedance: 1 MΩ \pm2% 20 pF. • Accessories: 2 \times passive probes (1X/10X). 	120000	ELECTRONICS AND COMMUNICATION ENGINEERING
3	112	1	DC VOLTMETER	DC Voltmeter ANALOG - Moving coil - (0-30)V, Class 1.0 (\pm 1 % full-scale) accuracy	700	ELECTRONICS AND COMMUNICATION ENGINEERING
		1		DC Digital Voltmeter - 3½ digit LED/LCD display, Measurement range: 0–20 V dc, Accuracy: Class 0.5(\pm 0.5 % full-scale)	700	
		1		DC Voltmeter ANALOG - Moving coil - (0-1)V, Class 1.0 (\pm 1 % full-scale) accuracy	700	

S. No	College Code	Quantity	Name of the Equipment (As per TANSCHÉ List)	Specification	Approximate Cost	Department where the equipment is required
4	112	1	DC AMMETER	DC Ammeter Analog - Moving coil - (0 -500) mA, Class 1.0 (±1 % full-scale) accuracy	700	ELECTRONICS AND COMMUNICATION ENGINEERING
		1		DC Ammeter ANALOG – Moving coil - (0-30) mA, Class 1.0 (±1 % full-scale) accuracy	700	
		1		DC Ammeter Analog - Moving coil - (0 - 1000) µA, Class 1.0 (±1 % full-scale) accuracy	700	
		1		DC Ammeter DIGITAL - 3½ digit LED/LCD display, Measurement range: 0–5 A dc, Accuracy: Class 0.5 (±0.5 % full-scale)	700	
5	112	1	LVDT Sensor Trainer Kit	<ul style="list-style-type: none"> • AC mains power supply: 230 V ±10%, 50 Hz. • Fixed regulated DC power supply: +5 V, ±12 V / 500 mA. • Transducer: LVDT. • LVDT displacement: ±20 mm (40 mm total). • Displacement scale: Linear ±20 mm. • Displacement display: Red color 3½ digit LED display. • Excitation frequency: 4 kHz. • Excitation voltage: AC variable excitation voltage up to 5 V. 	35000	ELECTRONICS AND COMMUNICATION ENGINEERING
6	112	1	Auto Transformer	<ul style="list-style-type: none"> • Input: 230–240 V ac, 50 Hz. • Output: Variable (0–270) V ac. • Rating: 1 kVA. • Insulation Class: Class F / Class H. • Construction: Copper winding with carbon brush arrangement. • Mounting: Bench-top / panel type. • Protection: Suitable fuse / overload protection. 	5000	ELECTRONICS AND COMMUNICATION ENGINEERING
7	112	2	FPGA / SoC Development Board	<ul style="list-style-type: none"> • FPGA fabric: mid-range, ~85K–100K logic cells, 220+ DSP slices. • Integrated dual-core ARM Cortex-A processor. • On-board DDR3/DDR3L memory ≥ 512 MB. • Peripherals: USB, Ethernet, HDMI (in/out), general-purpose I/O. • Expansion: Pmod and standard headers for sensors/modules. • Programming & Debug: USB JTAG, microSD support. • Power: 5 V or 12 V dc input. • Supports HDL (Verilog/VHDL), C/C++, embedded Linux, Python. 	67750	ELECTRONICS AND COMMUNICATION ENGINEERING
TOTAL					304850	(Three lakhs four thousand eight hundred fifty rupees only)